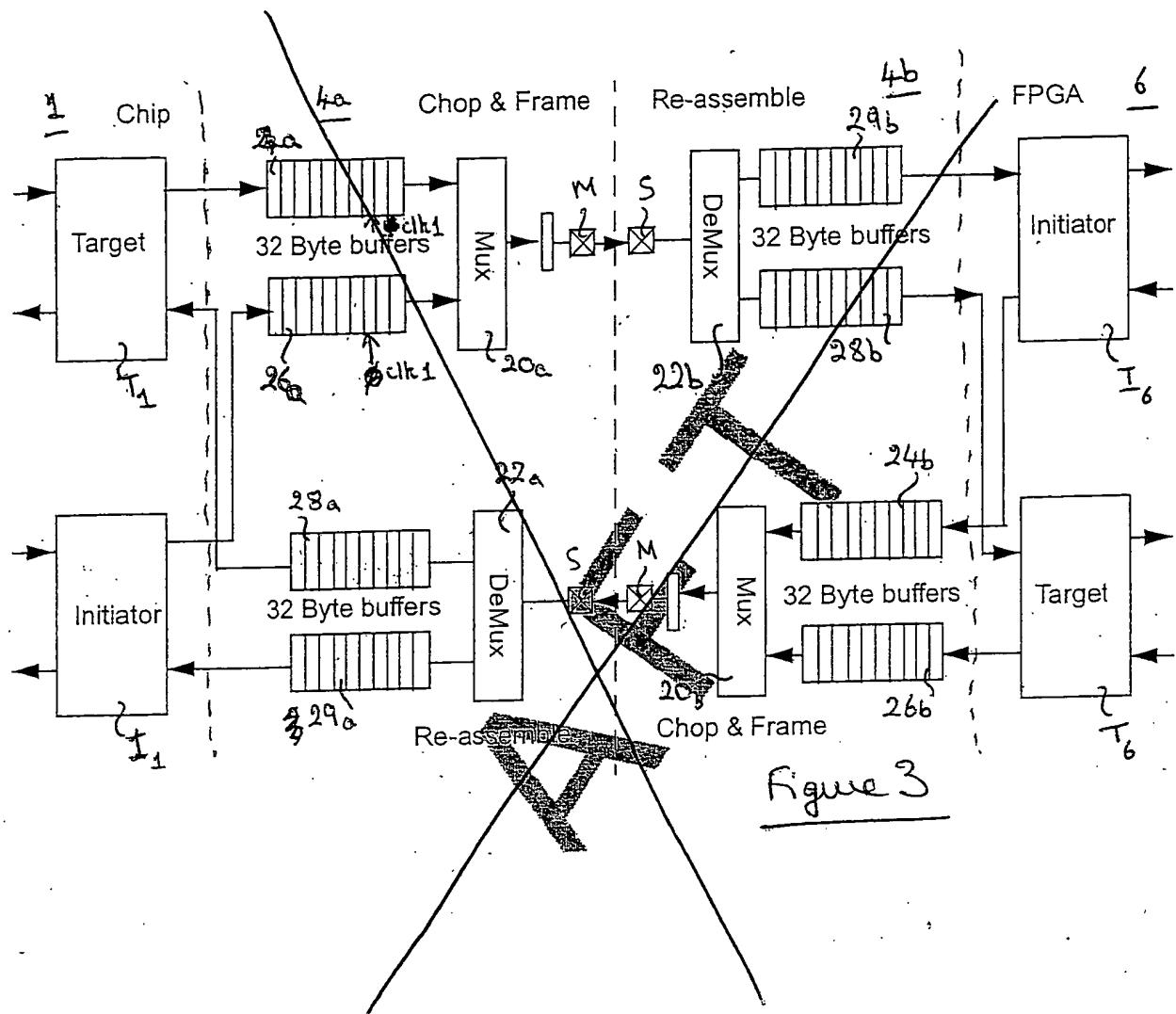
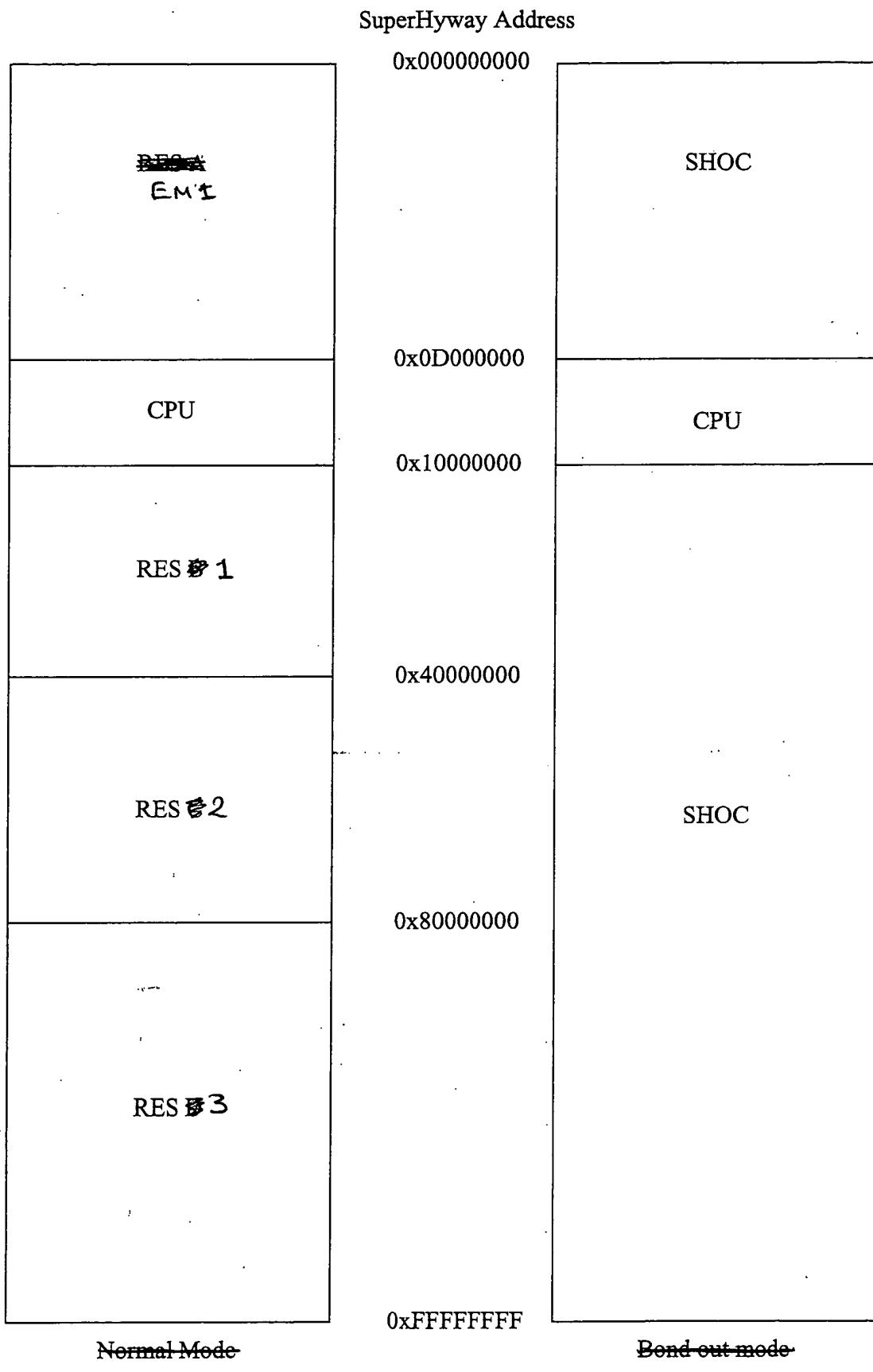


Figure 1

Integrated system





Normal Mode

Figure 3

Dual Address maps

Bond-out mode

Figure 4

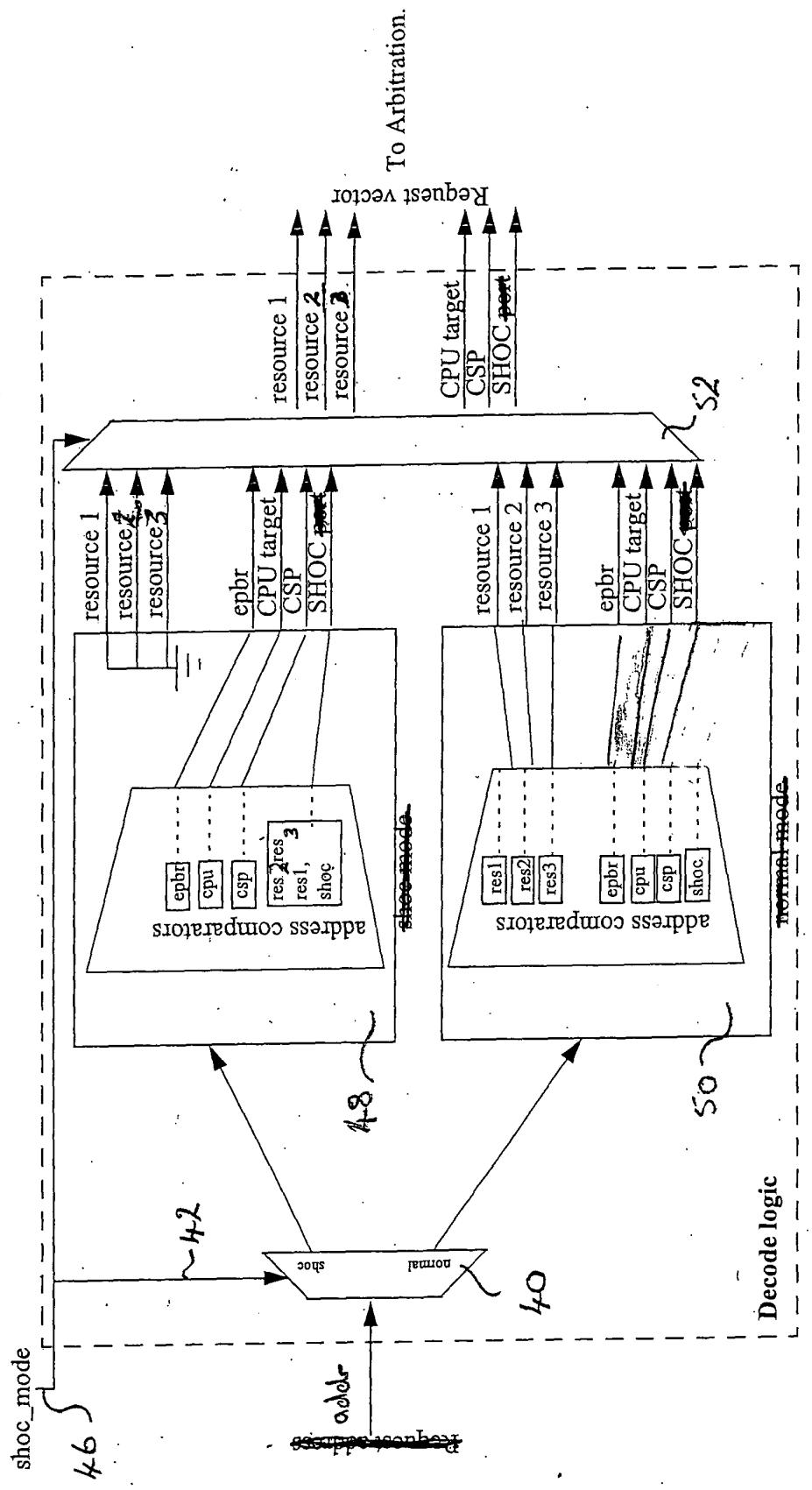


Figure 5